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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Benyong Zhang
Application No. : 10/694,450
Filed : October 27, 2003
For : METHOD AND SYSTEM FOR PROVIDING A PHASE-
LOCKED LOOP WITH REDUCED SPURIOUS TONES
Group No. : 2816
Examiner : Minh T. Nguyen

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents and publications herein are listed below and on the attached Forms PTO/SB/08A & B. Copies of the listed publications are submitted herewith.

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
3,928,813	Kingsford-Smith	December 23, 1975
4,204,174	King	May 20, 1980
4,206,425	Nossen	June 3, 1980

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<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
4,609,881	Wells	September 2, 1986
4,815,018	Reinhardt et al.	March 21, 1989
5,038,120	Wheatley et al.	August 6, 1991
5,124,670	Lawton	June 23, 1992
5,920,233	Denny	July 6, 1999

Publications

Tadao Nakagawa et al., "A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI", IEEE Transactions on Microwave Theory and Techniques, Vol. 42, No. 12, December 1994, Pp. 2579-2582.

Tom A. D. Riley et al., "Delta-Sigma Modulation in Fractional- N Frequency Synthesis", IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, Pp. 553-559.

Brian Miller et al., "A Multiple Modulator Fractional Divider" Forty-Fourth Annual Symposium on Frequency Control, IEEE, 1990, Pp. 559-568.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

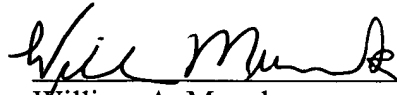
This Information Disclosure Statement is being transmitted after the mailing date of the first Office Action on the merits. Therefore, Applicant encloses a check in the amount of \$180.00 for the Information Disclosure Statement filing fee.

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APPLICATION NO.: 10/694,450
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Respectfully submitted,

DAVIS MUNCK, P.C.

Date: Aug. 3, 2005



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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Complete if Known

Application Number	10/694 450
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Filing Date	October 27, 2003
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First Named Inventor	Benyong Zhang
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Art Unit	2816
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Examiner Name	Minh T. Nguyen
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Attorney Docket Number	P05719
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	10/694,450
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First Named Inventor	Benyong Zhang
Art Unit	2816
Examiner Name	Minh T. Nguyen
Attorney Docket Number	P05719

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BA	Tadao Nakagawa et al., "A Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses a New Pulse Generator LSI", IEEE Transactions on Microwave Theory and Techniques, Vol. 42, No. 12, December 1994, Pp. 2579-2582.	
	BB	Tom A. D. Riley et al., "Delta-Sigma Modulation in Fractional-N Frequency Synthesis", IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, Pp. 553-559.	
	BC	Brian Miller et al., "A Multiple Modulator Fractional Divider" Forty-Fourth Annual Symposium on Frequency Control, IEEE, 1990, Pp. 559-568.	

Examiner
SignatureDate
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